

Control System

This invention relates to a control system for a load such as a drive mechanism and in particular to a so-called "fail off" control system in which, when a fault 5 is detected, the operation of the load is ceased or switched out.

Control systems generally include fault detection systems which control the operation of the control system when a fault is detected. There are three main types of control system with fault detection: "fail on", in which the mechanism associated with the control system is maintained in an "on" state if 10 a fault is detected (commonly used in situations (e.g. aircraft) in which to turn the system off may result in fatal consequences); "fail off" in which the mechanism associated with the control system is put into an "off" state if a fault is detected (commonly used in situations (e.g. vehicle drive mechanisms) in which to leave the system on may result in fatal consequences); and "do 15 nothing", in which the mechanism associated with the control system is maintained in its current state if a fault is detected and a log of a fault generated for later inspection and solution.

The invention will now be described, by way of example only, with reference 20 to the accompanying drawing, in which:

Figure 1 is a first embodiment of a drive control system.

A method and apparatus for controlling a load is described. In the following description, for the purposes of explanation, numerous specific details are set 25 forth to provide a thorough understanding of the present invention. It will be apparent to a person skilled in the art that the present invention may be practised without these specific details. In other instance, well-known structures and devices are shown in block diagram form to avoid unnecessarily obscuring the present invention.

The needs identified above and other needs and objects that will become apparent from the following description are achieved in the present invention which comprises, in one aspect, a control system for a load, the system comprising a first microprocessor having an output to drive one side of a load and a second microprocessor having an output to drive the other side of the load. The system is arranged so that when either microprocessor detects a fault in the control of the load the load is switched off. In other aspects, the invention encompasses apparatus for carrying out the foregoing steps.

10 The control system to be described is part of the electronic system used in a vehicle such as a car but the method is applicable to other electronic systems, especially those that require fail-off systems.

15 Figure 1 shows a first embodiment of a drive control system. A load 10 is driven by two drivers, a high side driver 12 and a low side driver 14. The operation of the driver 12 is controlled by a first microprocessor 16 while driver 14 is controlled by a second microprocessor 18. For safety reasons the second microprocessor 18 is provided to monitor the operation of the drive control system. The drivers 12, 14 may take any suitable form e.g. MOSFET switches or the like. The drivers 12, 14 may drive the load 10 by various means 20 such as a Pulse Width Modulation (PWM) signal or the like.

25 Two microprocessors are provided to ensure a fail-safe operation of the drive control system. In normal operation, the main microprocessor 16 controls the high side driver 12 (the low side driver 14 normally being switched on) and monitors the operation of the load by monitoring the low side of the load 10 at point A. If it detects a fault it can switch off the load via driver 12.

Each microprocessor 16, 18 has programmed into it a set of rules by which the operation of the load is controlled. The set of rules of the second microprocessor 18 may be identical to the set of rules of the first microprocessor. Alternatively, the set of rules of the second microprocessor 18 5 may be coarser than or a subset of the set of rules of the first microprocessor.

In use, the second microprocessor 18 monitors the operation of the first microprocessor 16 and the operation of the load 10 to determine if the system is operating according to the set of rules of the second microprocessor. If either 10 is not operating according to the set of rules of the second microprocessor, the microprocessor 18 switches out the load 10 by means of setting the low side driver 14 to open. Thus the load no longer has any effect on other systems.

In a preferred implementation of the invention, the second microprocessor 15 monitors the outputs of the first microprocessor that controls driver 12 to monitor for faults with the main microprocessor 16. For this purpose, a connection 19 is provided. A resistor R1 is also provided to ensure that a fault in microprocessor 18 is unable to turn on driver 12 (ie R1 must be relatively high compared with R2 and the output resistance of the driver within 20 microprocessor 16).

Each time a microprocessor is powered up, the microprocessor is reset which usually involves most, if not all, of the pins of the microprocessor being re-set as inputs. The programming of the microprocessor then resets the pins to their 25 required state for proper operation. If the microprocessor incorrectly sets a pin to be an input rather than an output (or vice versa) clearly a fault with the microprocessor will exist.

In the arrangement of Figure 1 resistor R2 operates to ensure that if the output pin of the main microprocessor 16 that is connected to the high side driver 12 switches to become an input pin, then the high side driver 12 is switched to open and the effect of the load switched out. Resistor R2 is connected between the ground rail and the output pin of the main microprocessor that is connected to the high side driver 12. R3 is connected between the ground rail and the output pin of the monitor microprocessor 18 that is connected to the low side driver 14 and ensures that driver 14 is switched off while the driver output pin of microprocessor 18 is high impedance.

10

The control system also includes a current sensor 20. This in itself may be a potential fault source since if it fails then the microprocessors are unable to detect this failure. This may be overcome by providing two current sensors in series. Alternatively, in a preferred embodiment of the invention, a back-up to the current sensor is provided by the monitor microprocessor 18 which calculates the current from the power supply voltage and the resistance of the load 10 by means of the equation $I = V/R$. This may also be achieved by monitoring the voltage at the high side driver 12 and the voltage at the low side driver 14, calculating the voltage drop across the load and, knowing the resistance of the load, calculating the load current.

The results of the calculation may then be compared with the output of the current sensor 20 and if the difference between the two meets predetermined criteria (e.g. is less than or equal to a pre-determined threshold), then the monitor microprocessor 18 detects a fault with the current sensor and either switches out the load as a result (for a fail off system) or logs the fault for subsequent consideration. In the latter case, the control system would then rely on the current calculation to monitor the current which may not be desirable, depending upon the type of load and/or the field of application of the load.

The control system shown is applicable to many areas where the control of a drive is required. The invention has particular application to a gear control system, for instance as used in a vehicle, but this is not intended to be limiting.

5 In the field of vehicular gear control systems, the load 10 may be a gear box selector, a clutch selector, a valve in a pneumatics system etc.

In the foregoing specification, the invention has been described with reference to specific embodiments thereof. It will however be evident that various

10 modifications and changes may be made thereto without departing from the broader spirit and scope of the invention. The description and drawings are, accordingly, to be regarded in an illustrative rather than a restrictive sense.